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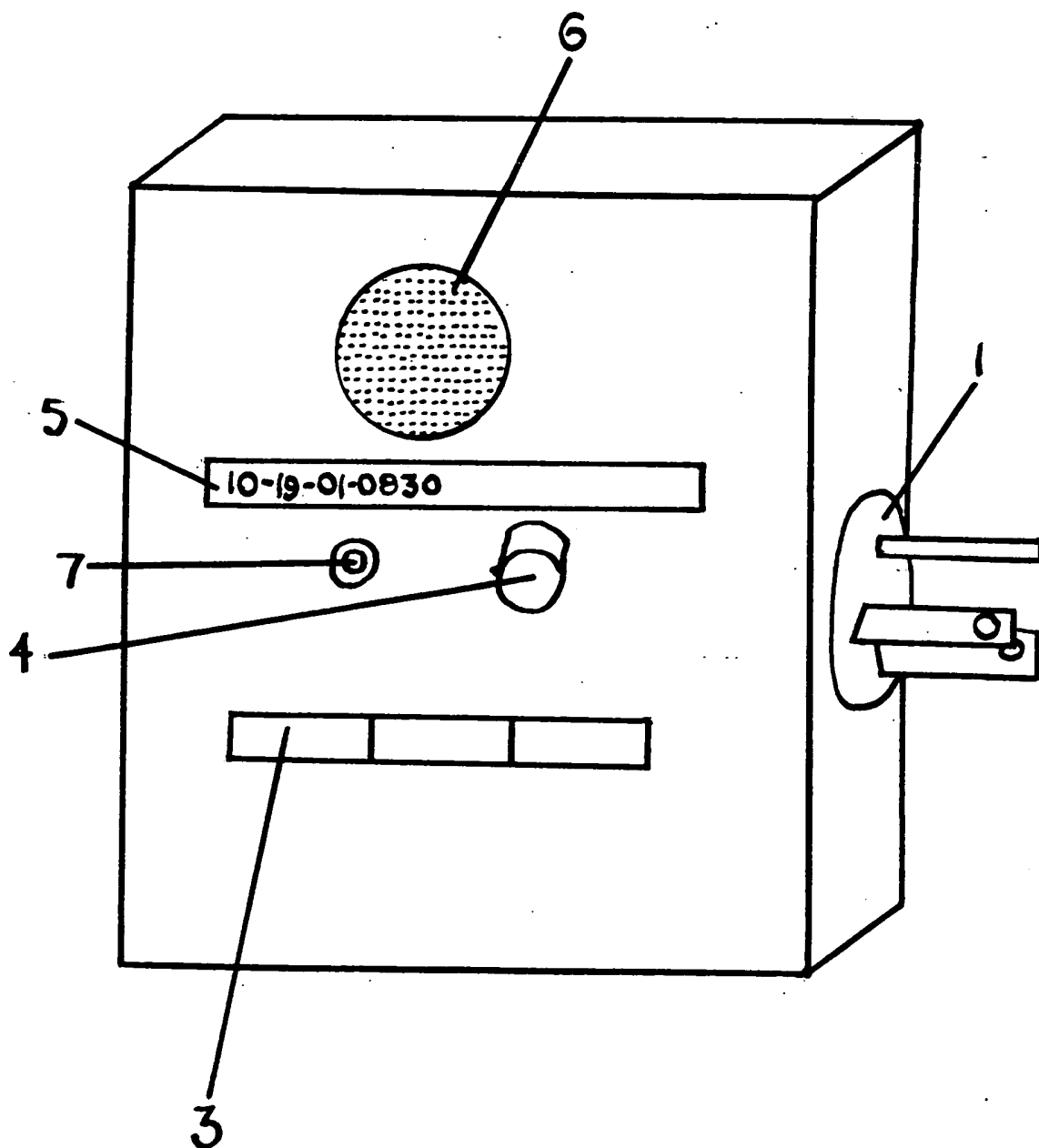


FIG. 1

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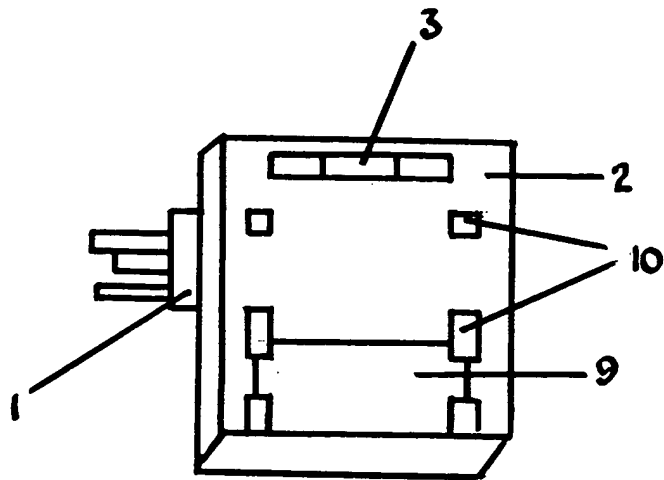
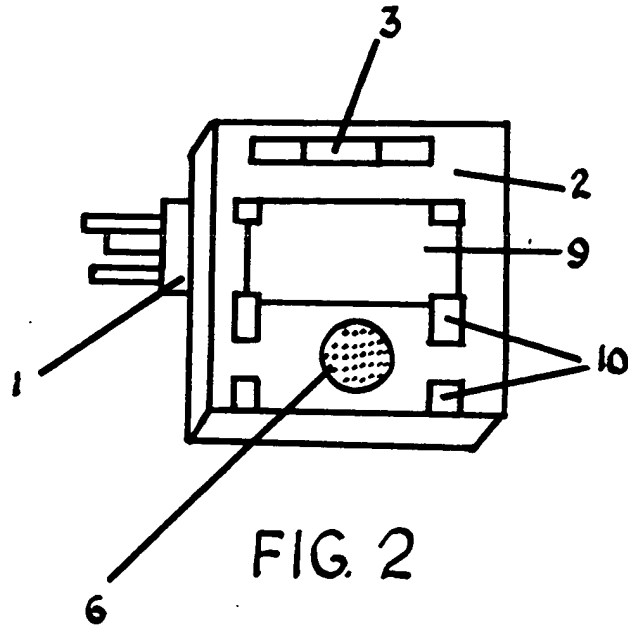
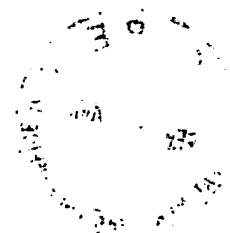


FIG. 3

A schematic diagram of a device. It consists of a central rectangular block labeled '2'. To the left of block '2', there are three horizontal lines representing inputs, labeled '1'. To the right of block '2', there is a single horizontal line representing an output, labeled '12'. A small rectangular component is positioned above block '2', with a line connecting it to the output line '12'.

FIG. 5



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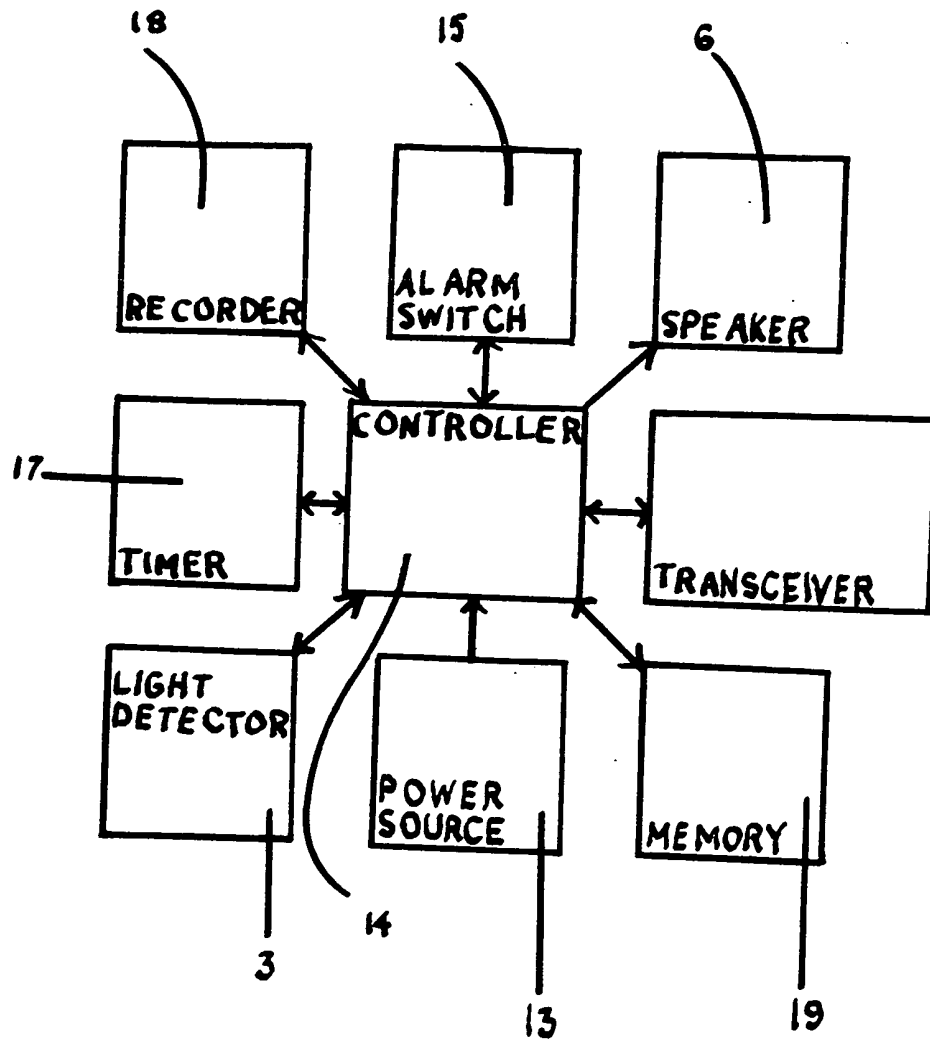


FIG. 6